

asynchronously operating logic component to a second selected asynchronously operating logic component in a series of operating steps.

D1
comp.
D2
7. The data carrier as claimed in Claim 1, wherein the predetermined time frame is a clock signal.

REMARKS

Claims 1-6 stand rejected under 35 U.S.C. 103(a) as being unpatentable over Asami et al., U.S. Patent No. 6,036,100, (hereinafter "Asami") in view of Commercial (WO99/23550). Claims 1-6 are in the application. Applicants respectfully submit that the pending claims, as amended, are patentable for at least the following reasons.

Claim 1 is directed to a data carrier comprising a data processing unit having a plurality of asynchronously operating logic components; and at least one contactless interface configured to enable coupling to a read/write apparatus in order to receive electrical energy for operation of the data processing unit, wherein selected asynchronously operating logic components are activated in response to a request signal in a coordinated manner using the received electrical energy and without using a predetermined time frame from the read/write apparatus.

Asami, as read by the applicants, relates to a noncontact IC which transmits and receives data to and from a host computer using RF signals has a buffer for storing received data temporarily and a control circuit for controlling operation of the buffer main memory thereof, wherein the control circuit starts processing data stored in the buffer only when no further data is input after a predetermined data receiving time period has elapsed from the latest data input to the buffer.

Commercial, as read by the applicants, relates to a microprocessor comprising a means for concatenating bits

Asami and Commercial, fail, either alone or in combination, to teach, show or disclose that selected asynchronously operating logic components are activated in response to a request signal in a coordinated manner using the received electrical energy and without using a predetermined time frame from the read/write apparatus, as specifically recited in amended independent claim 1.

Since Asami and Commercial does not teach, show or suggest all of the features of amended independent claim 1, as recited above, applicant respectfully submits that this claim is patentable over these references.

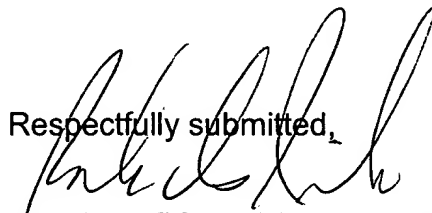
The other claims in this application are each dependent from the independent claim discussed above and are therefore believed patentable for the same reasons. Since each dependent claim is also deemed to define an additional aspect of the invention, however, the individual consideration of the patentability of each on its own merits is respectfully requested.

The applicants submit that the claims, as amended, fully satisfy the requirements of 35 U.S.C. 103. In view of the foregoing remarks, entry of this amendment, favorable reconsideration and early passage to issue of the present application are respectfully solicited.

Applicants' undersigned attorney may be reached by telephone at the number given below.

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APPENDIX A
VERSION WITH MARKING TO SHOW CHANGES MADE
IN THE CLAIMS

Please amend the claims as follows and add new claim 7:

1. (Amended) A data carrier comprising:
 a data processing unit, having ~~wherein the data processing unit~~
~~being constructed a plurality of at least mainly of at least substantially~~
asynchronously operating logic components; and

 at least one contactless interface, configured to enable ~~wherein the~~
~~data processing unit can be coupled~~ to a read/write apparatus in order to
~~exchange data signals and to take up~~ receive electrical energy for operation of
the data processing unit,

 wherein selected asynchronously operating logic components are
activated in response to a request signal in a coordinated manner using the
received electrical energy and without using a predetermined time frame from the
read/write apparatus.
2. The data carrier as claimed in Claim 1, wherein the contactless
interface and the data processing unit are coupled to one another via an
asynchronous transmission/receiving circuit which is included in the data
processing unit.
3. The data carrier as claimed in Claim 1, wherein individual stages
within at least the data processing unit operate in a time interleaved manner.
4. The data carrier as claimed in Claim 1, wherein the contactless
interface for the electrical energy for the operation of the data processing unit has
the function of an at least substantially ideal current source.

5. The data carrier as claimed in Claim 1, wherein the coordinated manner includes an activated selected asynchronously operating logic component providing a finished message after executing its operation, the finished message operable as a request message to another selected asynchronously operating logic component.

6. The data carrier as claimed in Claim 1, wherein the coordinated manner includes the propagation of a request message from a first selected asynchronously operating logic component to a second selected asynchronously operating logic component in a series of operating steps.

7. (New) The data carrier as claimed in Claim 1, wherein the predetermined time frame is a clock signal.